

Electronic Patent Application Fee Transmittal

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|--|--|-----------------|---------------|-----------------------------|
| Application Number: | 10815903 | | | |
| Filing Date: | 31-Mar-2004 | | | |
| Title of Invention: | STRATEGY TO VERIFY ASYNCHRONOUS LINKS ACROSS CHIPS | | | |
| First Named Inventor/Applicant Name: | Debendra Das Sharma | | | |
| Filer: | Edwin Taylor/In Chung | | | |
| Attorney Docket Number: | 42P18579 | | | |
| Filed as Large Entity | | | | |
| Utility under 35 USC 111(a) Filing Fees | | | | |
| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
| Basic Filing: | | | | |
| Pages: | | | | |
| Claims: | | | | |
| Miscellaneous-Filing: | | | | |
| Petition: | | | | |
| Patent-Appeals-and-Interference: | | | | |
| Post-Allowance-and-Post-Issuance: | | | | |
| Utility Appl issue fee | 1501 | 1 | 1510 | 1510 |
| Publ. Fee- early, voluntary, or normal | 1504 | 1 | 300 | 300 |

| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
|--------------------|----------|----------|--------|----------------------|
| Extension-of-Time: | | | | |
| Miscellaneous: | | | | |
| Total in USD (\$) | | | | 1810 |